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AUG 24 2005

U.S. PATENT & TRADEMARK OFFICE

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT

(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.

SEC.1066

Re Application of:

Duk-Min Yi et al.

Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
10/797,604	03/11/2004	Kevin V. Quinto	20987	2826	3882

Title:

MOS TRANSISTOR HAVING MESH-TYPE GATE ELECTRODE

Address to:

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

37 CFR 1.97(b)

1. ☐ The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

37 CFR 1.97(c)

2. ☒ The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:

☐ the statement specified in 37 CFR 1.97(e);

OR

☒ the fee set forth in 37 CFR 1.17(p).

08/25/2005 SZEWDIE1 00000019 10797604

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**TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT**  
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(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

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Dated: **AUGUST 24, 2005**

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**Complete if Known**

Application Number	10/797,604
Filing Date	03/11/2004
First Named Inventor	Duk-Min Yi et al.
Art Unit	2826
Examiner Name	Kevin V. Quinto
Attorney Docket Number	SEC.1066

## U. S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

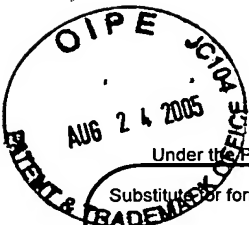
FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				
	B	JP 55113378	09/01/1980	Hitachi Ltd		<input checked="" type="checkbox"/>
	C	JP 11261056	09/24/1999	Toshiba Corp		<input checked="" type="checkbox"/>
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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

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Application Number	10/797,604
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First Named Inventor	Duk-Min Yi et al.
Art Unit	2826
Examiner Name	Kevin V. Quinto
Attorney Docket Number	SEC.1066

Sheet 2 of 2

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	D	"Field-effect transistor output driver" IBM TECHNICAL DISCLOSURE BULLETIN, IBM CORP. NEW YORK, US, vol. 28, no. 8, 1986, pages 3497-3498, XP002088269	✓

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